

CLAIMS

1. (Currently amended) A thin film transistor array panel comprising:
an insulating substrate;
a plurality of thin film transistors formed on the substrate;
a plurality of three primary color filters formed on the substrate;
a plurality of first pixel electrodes formed on the color filters and connected to the thin film transistors; and
a plurality of second pixel electrodes formed on the substrate and connected to the thin film transistors, wherein the second pixel electrodes do not overlap the color filters; and
an organic insulating layer including a plurality of first portions disposed between the color filters and the first pixel electrodes and a plurality of second portions disposed under the second pixel electrodes and having thickness larger than the first portions.
2. (Canceled).
3. (Original) The panel of claim 2, further comprising an inorganic insulating layer disposed between the color filters and the thin film transistors or between the organic insulating layer and the thin film transistors.
4. (Original) The panel of claim 1, further comprising a plurality of transparent filters disposed under the second pixel electrodes.
5. (Original) The panel of claim 4, wherein the transparent filters includes transparent photosensitive material or acrylic material.
6. (Previously presented) The panel of claim 2, further comprising an inorganic insulating layer disposed between the color filters and the thin film transistors or between the transparent filters and the thin film transistors.
7. (Previously presented) The panel of claim 1, wherein the three primary colors include red, green and blue, the first pixel electrodes include third, fourth and fifth pixel

electrodes, and the red, green and blue color filters are located under the third, fourth, and fifth pixel electrodes, respectively.

8. (Previously presented) The panel of claim 7, wherein the first and the second pixel electrodes are sequentially arranged.

9. (Original) The panel of claim 7, wherein the first and the second pixel electrodes are arranged in a plurality of 2×3 matrices, each 2×3 matrix having a first row including third, fifth and fourth pixel electrodes arranged in sequence and a second row including fourth, second and third pixel electrodes arranged in sequence.

10. (Original) The panel of claim 7, wherein the first and the second pixel electrodes are arranged in a plurality of 2×2 matrices, each 2×2 matrix having a first row including third and fourth pixel electrodes arranged in sequence and a second row including fifth and second pixel electrodes arranged in sequence.

11. (Currently amended) A liquid crystal display comprising:

- a first substrate;
- a plurality of gate lines formed on the first substrate;
- a gate insulating layer formed on the gate lines;
- a semiconductor layer formed on the gate insulating layer;
- an ohmic contact layer formed on the semiconductor layer;
- a plurality of data lines formed on the gate insulating layer and intersecting the gate lines to define a plurality of pixel areas;
- a first protective layer formed on the data lines;
- a plurality of red, green and blue color filters formed on the first protective layer;
- a second protective layer formed on the color filters;
- a plurality of pixel electrodes formed on the second protective layer, the electrodes being connected to the data lines through the semiconductor layer;
- a second substrate facing the first substrate;
- a common electrode formed on the second substrate; and
- a liquid crystal layer interposed between the first substrate and the second substrate,

wherein the pixel areas include a plurality of white pixel areas having no red, green, blue or white color filters, and each of the white pixel areas having one of the pixel electrodes and wherein the second protective layer includes a plurality of first portions disposed on the color filters and a plurality of second portions disposed on the white pixel areas, wherein the second portions are thicker than the first portions.

12. (Original) The liquid crystal display of claim 11, wherein the liquid crystal layer has a vertical alignment with respect to the first and the second substrates.

13. (Original) The liquid crystal display of claim 12, further comprising a plurality of protrusions formed on the common electrode and made of organic material, wherein the pixel electrodes have cutouts.

14. (Original) The liquid crystal display of claim 11, wherein the liquid crystal layer has a twisted alignment.

15. (Canceled).

16. (Currently amended) A liquid crystal display comprising:
a first substrate;
a plurality of gate lines formed on the first substrate;
a gate insulating layer formed on the gate lines;
a semiconductor layer formed on the gate insulating layer;
an ohmic contact layer formed on the semiconductor layer;
a plurality of data lines formed on the gate insulating layer and intersecting the gate lines to define a plurality of pixel areas;
a first protective layer formed on the data lines;
a plurality of red, green, blue and transparent color filters formed on the first protective layer;
a second protective layer formed on the color filters;
a plurality of pixel electrodes formed on the second protective layer, the electrodes being connected to the data lines through the semiconductor layer, each of the pixel electrodes being formed on the red, green, blue, and transparent color filters, respectively;

a second substrate facing the first substrate;
a common electrode formed on the second substrate; and
a liquid crystal layer interposed between the first substrate and the second substrate, wherein the pixel electrodes formed on the blue filter or transparent filter has a smaller area than the pixel electrodes formed on the red or green filters.

17. (Original) The liquid crystal display of claim 16, wherein the liquid crystal layer has a vertical alignment with respect to the first and the second substrates.

18. (Original) The liquid crystal display of claim 17, further comprising a plurality of protrusions formed on the common electrode and made of organic material, wherein the pixel electrodes have cutouts.

19. (Original) The liquid crystal display of claim 16, wherein the liquid crystal layer has a twisted alignment.

20. (Original) The liquid crystal display of claim 16, further comprising a black matrix disposed on the first substrate and defining the pixel areas.

21. (Currently amended) A liquid crystal display comprising:
a first substrate;
a plurality of thin film transistors formed on the first substrate;
a plurality of pixel electrodes connected to the thin film transistors, each pixel electrode having a first domain divider;
a second substrate facing the first substrate;
a common electrode formed on the second substrate;
a liquid crystal layer interposed between the first substrate and the second substrate;
wherein the pixel electrodes comprise white pixel electrodes that display a white color and have no color or white filters, red pixel electrodes that display a red color, green pixel electrodes that display a green color, and blue pixel electrodes that display a blue color and wherein the white or blue pixel electrodes have a smaller area than the red or green pixel electrodes.

22. (Previously presented) The liquid crystal display of claim 21, further comprising a second domain divider formed on the common electrode.

23. (Previously presented) The liquid crystal display of claim 22, wherein the first domain divider is a cutout of the pixel electrode and the second domain divider is a protrusion formed on the common electrode, and wherein the second domain divider is made of organic material.

24. (Previously presented) The array panel of claim 1, wherein the second pixel electrodes do not overlap any filters.

25. (Previously presented) The array panel of claim 24, further comprising an inorganic insulating layer disposed between the color filters and the thin film transistors.

26. (Previously presented) The array panel of claim 24, wherein the three primary colors include red, green and blue.

27. (Previously presented) The liquid crystal display of claim 16, wherein the second protective layer includes a plurality of first portions disposed on the color filters and a plurality of second portions disposed on the white pixel areas, wherein the second portions are thicker than the first portions.

28. (Previously presented) The liquid crystal display of claim 11, further comprising a black matrix disposed on the first substrate and defining the pixel areas.

29. (New) The panel of claim 1, wherein one of the first pixel electrodes formed on a blue one of the primary color filters has a smaller area than either of two of the first pixel electrodes formed on a red one or a green one of the primary color filters.

30. (New) The panel of claim 1, wherein the second pixel electrodes are formed over a transparent filter and wherein the second pixel electrodes have a smaller area than either of

two of the first pixel electrodes formed on the red one or the green one of the primary color filters.

31. (New) The liquid crystal display of claim 11, wherein the white pixel areas have a smaller area than pixel areas corresponding to the red or green color filters.